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JUL 23 2004

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Gloria Sikora	(Depositor's name)
<i>Gloria Sikora</i>	(Signature)
July 19, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/994,545	11/26/2001	Peter Rabkin	00939A-085300US	8177

TITLE OF INVENTION: **POLYSILICON LAYERS STRUCTURE AND METHOD OF FORMING SAME**

NON-VOLATILE MEMORY CELL WITH MULTI-POLYSILICON LAYER GATES (as amended)

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/22/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
IM, JUNGHWA M	2811	257-315000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor, Inc.

Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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- ☒ Advance Order - # of Copies 10

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(Authorized Signature) *[Signature]* (Date) July 8, 2004
 Barak Sani, Reg. # 45,088

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 02 FC:1504 300.00 DA
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